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Dual Digital Low IF Complex Receiver

Field of the Invention

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This invention relates to communications receivers, and more particularly to receivers capable of receiving wideband signals in a given mode such as Wide Band Code Division Multiple Access (WBCDMA) and also medium or narrow band signals for another mode such as Time

15 Division Multiple Access (TDMA), of which the European cellular telephone system GSM (Global System Mobile) is one example.

Background of the Invention

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Present communication systems, for example cellular telephone communication systems, operate on narrow or medium bandwidth technologies. The GSM cellular telephone system, a Time Division Multiple Access (TDMA) system, is one such medium bandwidth system.

25 As requirements for higher speed transmission for, for example, data or video transmission increase, wider band technologies are becoming more desirable. One such wideband technology for cellular communications is Wide Band Code Division Multiple Access (WBCDMA). As new technologies develop it is common to provide communications equipment

30 capable of operating on more than one communications system to facilitate the transition from one system to the other or to allow users access to the combined capacity and features of both systems. Thus there is a need for communications receivers capable of receiving signals from both narrow or medium bandwidth systems and also from wide

35 bandwidth communications systems.

Present efforts to provide a single wideband and medium band receiver have resulted in receiver designs which merely duplicate the receiver circuitry for each mode by providing different receiver data paths for

each mode. As an example, in the Wideband CDMA system currently being developed in Europe, Universal Mobile Telephone System (UMTS), the channel bandwidth is 3.84 MHz while the current European cellular system, GSM, has a bandwidth of 200 KHz. Both of these systems
 5 operate in an IQ modulation mode wherein information to be transmitted, after being appropriately encoded, is provided in In-phase and Quadrature-phase modulation components superimposed on a carrier signal producing a complex signal which is subsequently demodulated and decoded by the receiver to reveal the originally encoded
 10 information.

Currently these two modes are combined into one communications receiver unit by providing two different receiver circuits with different downconversion mixers, blocking filters, amplifiers, antialiasing filters,
 15 and analog to digital converters. Each of the receiver circuits is thus a separate receiver capable of receiving and decoding the I and Q components of the transmitted signal. Accordingly, it would be advantageous to provide a single communications receiver capable of operating in several modes and in which many elements of circuitry are
 20 usable in each mode, thus reducing the complexity of the receiver by reducing the number of circuits necessary in the receiver.

Since the bandwidth of the WBCDMA UMTS system is 3.84 MHz, each I and Q component of the WBCDMA signal will be in the range of
 25 $3.84\text{MHz}/2$. These wide bandwidths, given the required dynamic range of the transmitted signal, require the use of sophisticated data converters such as Flash or Pipelined data converters which, while quite fast thus allowing high sampling rates to accommodate code tracking to recover the transmitted code in a spread spectrum system, are not particularly
 30 suitable for the GSM mode since these data converters have high power consumption and have limited dynamic range (10 bits) while the GSM mode requires higher dynamic range (14 bits) but allows lower sampling rates. Lower power consumption, of course, is always desirable in portable or mobile communications equipment such as cellular
 35 telephones. The GSM mode allows the use of Sigma-Delta digital to analog converters or other converters which are easily programmable for different bandwidths, have wider dynamic range, and consume less power.

Summary of the Invention

5 The above and other advantages can be obtained by superimposing the sub-bands of a wideband channel into narrower bandwidth components called II, IQ, QI, QQ using a dual low Intermediate Frequency (IF) digital approach such that each component is capable of using common data converters that can be reused when a medium or narrow bandwidth
10 mode is selected.

This may be achieved by providing a dual mode communications receiver for detecting and demodulating radio signals carrying information which has been encoded and modulated onto a carrier of either wide or narrow
15 bandwidth for transmission, comprising means for subdividing the detected band into sub-bands and superimposing the sub-bands into a plurality of components with a bandwidth similar to the sub-band bandwidth, means for processing that portion of the information contained in each component separately, and means for combining the
20 processed information from the components to reconstruct the original information transmitted.

This can be accomplished as well by providing a method for operating a dual mode communications receiver for detecting and demodulating
25 radio signals carrying information which has been encoded and modulated onto a carrier of either wide or narrow bandwidth for transmission, comprising subdividing the detected signals into detected sub-bands, superimposing the detected sub-bands into a plurality of components with a bandwidth similar to the sub-band bandwidth,
30 processing that portion of the information contained in each component separately, and combining the processed information from the components to reconstruct the original information transmitted.

35 Brief Description of the Drawings

FIG. 1 is an overall block diagram of a communications receiver in accordance with the invention.

FIG. 2 is a spectrum representation of the input signal V_{in} .

FIG. 3a and FIG. 3b are spectrum representations of the input signal V_{in} with the real and imaginary parts separated.

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FIG. 4a, FIG. 4b, FIG. 4c, and FIG. 4d are spectrum representations of the components I_I , I_Q , Q_I , and Q_Q which are the signal V_{in} after mixing each of the sub-bands with $\sin(\omega t)$ or $\cos(\omega t)$ and filtering each of the components.

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FIG. 5a and FIG. 5b represent graphically the V_{out++} vector which corresponds to the reconstruction of sub-band0 and sub-band1, and the V_{out-+} vector which corresponds to the reconstruction of sub-band2 and sub-band3.

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FIG. 6 is a schematic diagram of a Combiner circuit according to the instant invention.

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FIG. 7 is an overall block diagram of a communications receiver in accordance with the invention showing the configuration of the receiver for reception of narrow or medium bandwidth signals.

Description of the Preferred Embodiment

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FIG. 1 is an overall block diagram of a communications receiver in accordance with the invention. , An input signal V_{in} , which is a complex signal representing the baseband spectrum which will be translated to a Radio Frequency or Intermediate Frequency channel as earlier described, having information applied by a modulator in a transmitter to produce the input signal having In-phase and Quadrature-phase components, is applied to an amplifier 100 the output of which is applied to two quadrature mixers or downconverters 102, 104. V_{in} has a bandwidth of BW . V_{in} is subdivided into four sub-bands.

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The IQ downconverters 102,104 downconverts either a Radio Frequency Signal (RF) or an Intermediate Frequency (IF) channel to DC spectrum

using, for example, either a Gilbert Type of Mixer or a Chopper Type of Mixer.

A Local Oscillator 105 drives two analog to digital converters 106 and 107 to produce quadrature analog outputs ($\sin(wt)$ and $\cos(wt)$), where $w=2\pi BW/4$ which in turn drive quadrature network 108 which divides by $\pi/2$ in order to deliver two RF or IF oscillator signals which are in quadrature to feed the two Local Oscillator ports of the quadrature mixers 102,104.

The outputs of mixers 102 and 104 then are quadrature representations of the input signal V_{in} at baseband or a low Intermediate Frequency (hereafter references to baseband may alternatively imply low IF, depending on receiver design).

The outputs of the mixers 102 and 104 are filtered by a first low pass blocking filter (capacitors) and then are applied respectively to further mixers (multipliers) 110, 112, 114, and 116 where the I and Q sub-band components of bandwidth $BW/2$ are further divided into four components, II, IQ, QI, and QQ, of bandwidth $BW/4$ which are amplified and filtered by low pass filters 118, 120, 122, and 124 for further blocking filtering, each having a bandwidth equal to or greater than $BW/4$.

The mixers are low frequency mixers/multipliers except the I and Q IF or RF to baseband mixers 102 and 104. The mixers/multipliers 110,112,114 and 116 could be implemented as multiplier DAC's with gain, where the digital input port has the digital representation of $\cos(wt)$ and $\sin(wt)$ applied and the analog input port is the filtered output of the mixers 102 and 104. The clock frequency of the multiplier DAC depends on the amount of blocking provided in front to avoid spurious response at the harmonics of this clock. Also, this multiplier DAC could be used to set an Automatic Gain Control (AGC) signal in the receiver.

The Low pass filters are generally based on active RC type of filter with programmable cut-off frequencies such that wide-band or medium band or narrow band can be filtered. Those filters have a bandwidth equal to

BW/4 which simplifies their design versus filters of bandwidth equal to BW/2 with similar blocking rejection requirements. This allows the filters to have a reduced number of poles required per filter that then introduces less group and amplitude ripple. Also, the programming cut-off frequency range is reduced.

The outputs of low pass filters 118, 120, 122, and 124 are subsequently applied to Analog to Digital converters 126, 128, 130, and 132, which may be Sigma-Delta A /D converters, and decimation filters 134, 136, 138, and 140 to produce component signals IIf, IQf, QIf, and QQf which will be described in detail later. The signals are gain and phase corrected and recombined at 142 to produce output signals Vout++ and Vout+- which are subsequently demodulated and decoded to produce a digital bit stream representing the information previously encoded onto the complex input signal by the modulator of the transmitter.

The Analog to Digital converters 126, 128, 130, and 132 are preferably Sigma-Delta type A /D modulators with a programmable oversampling ratio for various wideband or medium or narrow band signal components. Since the components II, IQ, QI, QQ have their bandwidths reduced to BW/4 each, this reduces the required oversampling clock frequency of oversampling for the Sigma-Delta for the same Dynamic Range. For example, in WBCDMA mode with BW=3.84 Mhz, the oversampling clock would be selected in the range of 26Mhz such that the oversampling ratio is in the range of $26\text{Mhz}/(3.84\text{Mhz}/4)=27.08$ for 10 bits of resolution, while in GSM mode, the oversampling clock would be 13Mhz for an oversampling ratio $13\text{Mhz}/0.2\text{khz}=65$ for 14 bits of resolution.

The Sigma-Delta modulators 126,128,130 and 132 generate digital streams oversampled at specified frequencies of oversampling. These digital outputs will contain shaped noise spectrum depending on the order and the type of Sigma-Delta modulator as is typical for this kind of modulator. The digital outputs are then digitally filtered to remove the shaped noise of the Sigma-Delta modulators and then decimated to a lower clock frequency for further processing by decimation digital filters 134,136,138 and 140 operating at lower clock rates to perform selectivity filtering. The digital outputs IIf, IQf, Qif, and QQf are then processed by

digital multipliers and adders in Combiner 142 in order to provide gain and phase imbalance correction and to generate four digital outputs components represented by vectors V_{out++} and V_{out+-} .

5 The bandwidths of analog filters 118, 120, 122, and 124, the Sigma-Delta converters 126, 128, 130, and 132, and the digital decimation filters 134, 136, 138, and 140 can also be made programmable to handle several bandwidth settings.

10 In this preferred embodiment as shown in FIG. 1 a receiver is described in which the input signal is divided into four components, but it is understood that a different number of components may be used depending upon the bandwidth of the input signal and the desired bandwidth of the components.

15 The operation of the receiver of FIG. 1 can best be described with reference to the spectrum representations of the signals at various points in the receiver as set forth in FIGs. 2-5. An input channel applied to amplifier 100 is subdivided into four sub-bands hereafter referred to as
20 sub0, sub1, sub2, and sub3 and referred to by reference numerals 160, 162, 164, and 166, respectively, in FIG. 2 which is a spectrum representation of V_{in} after having been divided into four sub-bands. Each of the sub-bands has a bandwidth of $BW/4$.

25 The RF channel on which those four sub-bands is based is downconverted from the RF or IF frequency to baseband in two components, I and Q, as the outputs of mixers 102 and 104, which are in quadrature phase relationship and where each component has a spectrum width of $BW/2$ (from 0 to $BW/2$).

30 If the vector $V_{in}=I+j.Q$ is the representation of the input channel at baseband, then V_{in} can be expressed as a sum of four vectors that correspond to the four sub-bands, e.g., $V_{in}=V_{in0}+V_{in1}+V_{in2}+V_{in3}$. Fig. 2 also shows this representation where V_{in0} corresponds to sub0, V_{in1}
35 corresponds to sub1, V_{in2} corresponds to sub2, and V_{in3} corresponds to sub3.

FIG. 3a and FIG. 3b show the spectrum of V_{in} represented in its constituent I and Q components (above the lines f). Thus are shown the real part of V_{in} , e.g., $I = I_0 + I_1 + I_2 + I_3$, shown, respectively, as 160I, 162I, 164I, and 166I in FIG. 3b, and the imaginary part $j.Q = j.Q_0 + j.Q_1 + j.Q_2 + j.Q_3$, shown, respectively, as 160Q, 162Q, 164Q, and 166Q in FIG. 3b.

As previously noted, then sub3, 166, is mixed with sub0, 160, sub2, 164, is mixed with sub1, 162, again looking separately at the I and j.Q spectra below the f lines.

Each component I and Q is then mixed with a low IF quadrature signals $\cos(wt)$ and $\sin(wt)$ in mixers 110, 112, 114, and 116 (where $w = 2\pi \cdot BW/4$ or a nearest value), i.e. the Low IF clock is half the I or Q bandwidth. The operation is as follows:

$$\begin{aligned} IIf &= II ** HII \text{ with } II = I \cos(wt) \\ IQf &= IQ ** HIQ \text{ with } IQ = I \sin(wt) \\ QIf &= QI ** HQI \text{ with } QI = Q \cos(wt) \\ QQf &= QQ ** HQQ \text{ with } QQ = Q \sin(wt) \end{aligned}$$

Where HII, HIQ, HQI and HQQ are the transfer function of the analog and digital filters 118, 120, 122, and 124, and 134, 136, 138, and 140, respectively, on the II, IQ, QI and QQ paths respectively.

The ** denotes as a time domain convolution.

$$\begin{aligned} II &= I \cdot (e^{+j(wt)} + e^{-j(wt)})/2 \\ IQ &= I \cdot (e^{+j(wt)} - e^{-j(wt)})/2j \\ QI &= Q \cdot (e^{+j(wt)} + e^{-j(wt)})/2 \\ QQ &= Q \cdot (e^{+j(wt)} - e^{-j(wt)})/2j \end{aligned}$$

Multiplying with $e^{+j(wt)}$ is equivalent to shift the spectrum by $+w$, i.e by $+BW/4$.

Multiplying with $e^{-j(wt)}$ is equivalent to shift the spectrum by $-w$, i.e by $-BW/4$.

The result is the spectrum shown in FIG. 4a, FIG. 4b, FIG. 4c, and FIG. 4d which also graphically denotes the outputs of each of the filters 118, 120, 122, and 124, respectively, as II, IQ, QI, and QQ, each shown in the FIGs. As a filter of bandwidth BW/2 and the outputs of filters 134, 136, 138, and 140 as IIf, IQf, QIf, and QQf, respectively, also shown as filters of bandwidth W/2. The spectrum shifts as described above are also shown graphically in FIGs. 4. Note that each of the outputs of the filters in the II, IQ, QI, and QQ paths contains each of the four sub-bands, that is why the components II, IQ, QI, and QQ have the four sub-bands superimposed, as shown in FIG.4a-FIG. 4d, and thus all the information originally contained within the input signal Vin is available to reconstruct the original signal, but the processing of the signals is at narrower bandwidths than the original signal Vin and thus can be performed with Sigma-Delta A/D converters rather than the flash or pipelined data converters usually used for this operation to support wide bandwidths.

To produce the output in the form of the reconstruction of the original (but now processed) information transmitted, the information of each of the four sub-bands must be recombined in Combiner 142 to form two vector components Vout++ and Vout-+:

$$\begin{aligned} V_{out++} &= (IIf - QQf) + j.(IQf + QIf) \\ V_{out-+} &= (IIf + QQf) + j.(-IQf + QIf) \end{aligned}$$

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Then the Vout++ vector corresponds to the reconstruction of sub-band0 and sub-band1, 160 and 162, and the Vout-+ vector corresponds to the reconstruction of sub-band2 and sub-band3, 164 and 166, as shown graphically in FIG. 5a and FIG.5b, respectively. The recombination by Combiner 142 can be performed in several ways, for example:

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- Use an adder/subtractor to perform $IIf - QQf = I_{++}$ for Vout++.
- Use an adder/subtractor to perform $IQf + QIf = Q_{++}$ for Vout++
- Use an adder/subtractor to perform $IIf + QQf = I_{-+}$ for Vout-+
- Use an adder/subtractor to perform $-IQf + QIf = Q_{-+}$ for Vout-+

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The operation $V_{out++}e^{j(wt)}$ is formed using a complex multiplier to perform the following digital operation: $(I_{++} + j.Q_{++}).(\cos(wt) - j.\sin(wt)) =$

5 $I_{++}.\cos(wt) + Q_{++}.\sin(wt) + j.(-I_{++}.\sin(wt) + Q_{++}.\cos(wt))$ using adders and multipliers.

The operation $V_{out-+}e^{j(wt)}$ is formed using a complex multiplier to perform the following digital operation: $(I_{-+} + j.Q_{-+}).(\cos(wt) + j.\sin(wt)) =$

10 $I_{-+}.\cos(wt) - Q_{-+}.\sin(wt) + j.(I_{-+}.\sin(wt) + Q_{-+}.\cos(wt))$ using adders and multipliers.

Note that all four of the components I_{if} , I_{qf} , Q_{if} , and Q_{qf} are required to reconstruct each sub-band. It is required that all four components be available to recover the sub-band separately. Each component (I_{if} , I_{qf} , Q_{if} , and Q_{qf}) contains within the $BW/4$ information related to all the four sub-bands as shown in their respective spectrum. That is what allows the receiver to process all four sub-bands in parallel and allows the use of more favorable A/D conversion techniques. For this reason the receiver can be referred to as a Double Cartesian receiver since the four

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20 components are required to reconstruct each sub-band.

Note also that those four real components have a spectrum width of $BW/4$ and that the same low pass filters with the same low pass A/D converters are required to digitize those four components.

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The receiver employing the instant invention could be seen as a receiver having an RF or IF input channel of bandwidth BW with four baseband output components (Double Cartesian) called I_{if} , Q_{qf} , Q_{if} , and I_{qf} that have a bandwidth requirement of $BW/4$. To reconstruct back the original spectrum, the two reconstructed vector V_{out++} and V_{out-+} needed to be shifted by $-w$ and by $+w$ respectively, and so are multiplied, respectively by $e^{-j(wt-\phi_0)}$ and $e^{+j(wt-\phi_0)}$:

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$$V_{out\ r} = V_{out++} \cdot e^{-j(wt-\phi_0)} + V_{out-+} \cdot e^{+j(wt-\phi_0)}$$

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Where ϕ_0 is chosen to remove phase discontinuities. Since $\sin(wt)$ and $\cos(wt)$ need to be generated, the phase generator that generated $\sin(wt)$ and $\cos(wt)$ simply starts with an offset of $-\phi_0$.

By replacing V_{out++} and V_{out-+} by their expressions we get:

$$V_{out\ r} = 2(I_{If} \cos(wt - \phi_0) + I_{Qf} \sin(wt - \phi_0)) \\ + 2j.(Q_{If} \cos(wt - \phi_0) + Q_{Qf} \sin(wt - \phi_0))$$

5 i.e., the reconstructed In-phase and Quadrature components are:

$$I_r = (I_{If} \cos(wt - \phi_0) + I_{Qf} \sin(wt - \phi_0))$$

$$Q_r = (Q_{If} \cos(wt - \phi_0) + Q_{Qf} \sin(wt - \phi_0))$$

10 A reconstruction implementation of Combiner 142 is shown in FIG. 6 to generate the real and imaginary output of the reconstructed vector $V_{out\ r}$ in accordance with the equations set forth above and also provide gain and phase correction for mismatches introduced by the four paths. The recombination equations $I_r = (I_{If} \cos(wt - \phi_0) + I_{Qf} \sin(wt - \phi_0))$ and $Q_r = (Q_{If} \cos(wt - \phi_0) + Q_{Qf} \sin(wt - \phi_0))$ implemented in FIG. 6 also use
15 gain and phase corrections between the components when they do not match among themselves.

I_{If} is multiplied by the gain correction term K_{ii} , I_{Qf} is multiplied by the gain correction term K_{iq} , Q_{Qf} is multiplied by the gain correction term K_{qq} and Q_{If} is multiplied by the gain correction K_{qi} by multipliers 170, 172, 174, and 176, respectively. The gain corrections terms are chosen
20 such the corrected terms are equal in amplitude.

It is still necessary to correct the phase between the I_{If} , I_{Qf} , Q_{If} , and Q_{Qf} . Rather than multiplying, for example, I_{If} by $\cos(wt - \phi_0)$, it is
25 multiplied by $\cos(wt - \phi_0 + \Phi_{IQ})$ in multiplier 178 where Φ_{IQ} is a phase value used to phase shift I_{If} versus I_{Qf} to compensate the phase mismatch between I_{If} and I_{Qf} . Similarly, the term Φ_{QI} is the phase correction to compensate the phase mismatch between Q_{If} and Q_{Qf}
30 applied in multiplier 180.

Once I_{If} and I_{Qf} are gain and phase matched, and Q_{If} and Q_{Qf} are gain and phase matched, (I_{If} v, I_{Qf}), the output of the adder 182, and (Q_{Qf} , Q_{If}), the output of the adder 184, must be matched in phase. This is
35 done by adding to the output of adder 182 a term K_{ri} which is the output of adder 184. This is explained simply by the equation:

$$\cos(wt+\text{PhiIR})=\cos(wt)\cos(\text{PhiIR})+\sin(wt).\sin(\text{PhiIR})$$

This means that to correct by a phase value PhiR , the imaginary part $\sin(wt)$ (adder 184 output) is taken and is multiplied by $\text{Kri}=\sin(\text{PhiIR})$ and added to the adder 182 output $\cos(wt).\cos(\text{PhiIR})$. The operation of
 5 mutliplication by $\cos(\text{PhiIR})$ may be done before the adder182 by changing $\text{Kii}'=\text{Kii}.\cos(\text{PhiIR})$ and $\text{Kiq}'=\text{Kiq}.\cos(\text{PhiIR})$ to save a mutliplier.

Therefore, $\text{Ir}=\text{Kii}.\cos(\text{PhiIR}).\text{Iff}.\cos(wt-\text{phi0}+\text{PhiIQ}) +$
 10 $\text{Kiq}.\cos(\text{PhiIR}).\text{IQf}.\sin(wt-\text{phi0}) + \text{Qr}.\text{Kri}$
 and $\text{Qr}= \text{Kqq}.\text{QQf}.\sin(wt-\text{phi0}-\text{PhiQI}) + \text{Kqi}.\text{QIf}.\cos(wt-\text{phi0})$

When the received signal has a narrow bandwidth or a medium
 bandwidth (both of which may be termed for purposes of this description
 15 as narrowband, since they are narrow relative to the wideband signal
 previously discussed), like $\text{BW}/2$ rather than BW (for example a GSM
 cellular telephone signal), then two of the component branches of the
 receiver can be switched OFF while the other two branches are left ON
 with the digital Local Oscillator set to 0 or used to set gain control (AGC)
 20 . (i.e, $\cos(wt)=1$ or $\cos(wt)=\text{AGC}$ and $\sin(wt)=0$). This then functions as a
 single cartesian receiver.

The receiver of FIG. 1 is shown in FIG. 7 in such a mode. As shown in
 FIG. 6, the input to the receiver of FIG. 7 is operated the same as that of
 25 FIG. 1, that is, the Local Oscillator 105, , and Local Oscillator
 quadrature generator 108 produce the same signals as earlier described,
 as do mixers 102 and 104 which serve to reduce the input channel to
 baseband and divide the spectrum into its I and Q quadrature
 components. In the receiver of FIG. 6, however, the IQ and QQ paths
 30 are disabled by multiplying the input signals thereto by 0. This allows
 only two paths to remain active, one path for each of the I and Q
 components of the complex, but narrow, or narrower, band input signal.

The active paths II and QI filter the input channel in analog low-pass
 35 filters 118 and 122, respectively, perform analog to digital conversions in
 A/D converters 126 and 130 (which may be Sigma-Delta converters), and

provide digital filtering in filters 134 and 138 to produce output signals Iout and Qout which are then employed to derive the originally encoded information encoded by the transmitter.

- 5 As can be seen, in this manner both wideband and narrow band signals can be processed by the same receiver components and duplication of elements and data paths is avoided.

- 10 As previously noted, in this preferred embodiment a receiver is described in which the input signal is divided into four components (or two components in the case of medium or narrow bandwidth signals), but it is understood that a different number of components may be used depending upon the bandwidth of the input signal and the desired bandwidth of the components.

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